

Interfacing PICmicro® MCUs to an LCD Module

Author: Mark Palmer
Code: Mark Palmer/Scott Fink
Microchip Technology Inc.

INTRODUCTION

This application note interfaces a midrange PICmicro device to a Hitachi® LM032L LCD character display module, with a two line by twenty character display. LCD modules are useful for displaying text information from a system. In large volume applications, the use of custom LCD displays becomes economical. The routines provided should be a good starting point for users whose applications implement a custom LCD. This source code should be compatible with the PIC16C5X devices, after modifications for the special function register initialization, but has not been verified on those devices.

OPERATION

The Hitachi LM032L LCD character display module can operate in one of two modes. The first (and default) mode is the 4-bit data interface mode. The second is the 8-bit data interface mode. When operating in 4-bit mode, two transfers per character / command are required. 8-bit mode, though easier to implement (less program memory) requires four additional I/O lines. The use of 8-bit mode is strictly a program memory size vs. I/O trade-off. The three most common data interfaces from the microcontroller are:

1. An 8-bit interface.
2. A 4-bit interface, with data transfers on the high nibble of the port.
3. A 4-bit interface, with data transfers on the low nibble of the port.

The LCD module also has three control signals, Enable (E), Read/Write (R_W), and Register Select (RS). The function of each control signal is shown in Table 1.

TABLE 1: CONTROL SIGNAL FUNCTIONS

Control Signal	Function
E	Causes data/control state to be latched Rising Edge = Latches control state (RS and R_W) Falling Edge = Latches data
RS	Register Select Control 1 = LCD in data mode 0 = LCD in command mode
R_W	Read / Write control 1 = LCD to write data 0 = LCD to read data

A single source file, with conditional assembly is used to generate each of these three options. This requires two flags. The flags and their results are shown in Table 2.

TABLE 2: CONDITIONAL ASSEMBLY FLAGS

Flags		Result
Four_bit	Data_HI	
1	0	4-bit mode. Data transferred on the low nibble of the port.
1	1	4-bit mode. Data transferred on the high nibble of the port.
0	x	8-bit mode.

AN587

Figure 1, Figure 2, and Figure 3 show the block diagrams for the three different data interfaces. The LCD_CNTL and LCD_DATA lines are user definable to

their port assignment. This is accomplished with EQUate statements in the source code. See Appendices B, C, and D.

FIGURE 1: 8-BIT DATA INTERFACE

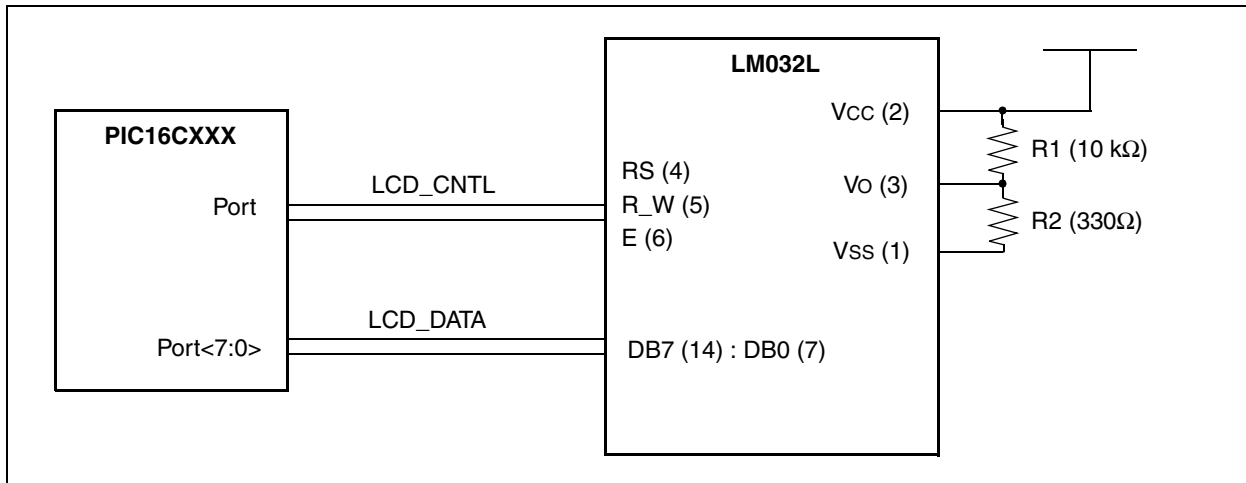


FIGURE 2: 4-BIT MODE; DATA TRANSFERRED ON THE HIGH NIBBLE OF THE PORT

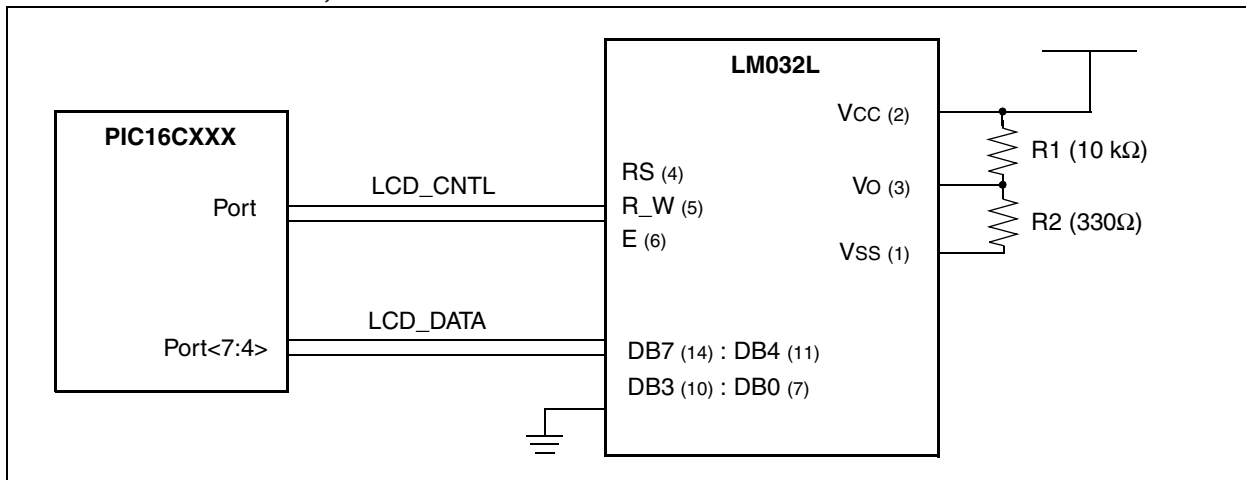
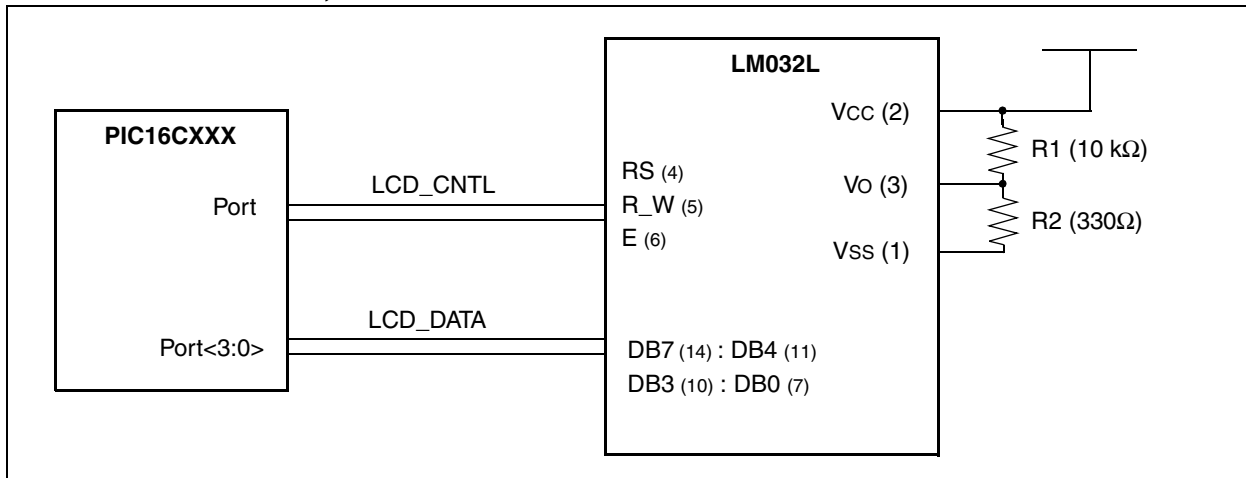


FIGURE 3: 4-BIT MODE; DATA TRANSFERRED ON THE LOW NIBBLE OF THE PORT



LCD's (drivers) are slow devices when compared to microcontrollers. Care must be taken from having communication occur too quickly. The software will need to control communication speed and timing to ensure the slow LCD and fast microcontroller can stay synchronized. The timing requirements of the LM032L are shown in Appendix A. We recommend that the complete specifications of the LM032L be acquired from Hitachi or a Hitachi distributor. The literature numbers are CE-E613Q and M24T013 for a LM032L display driver.

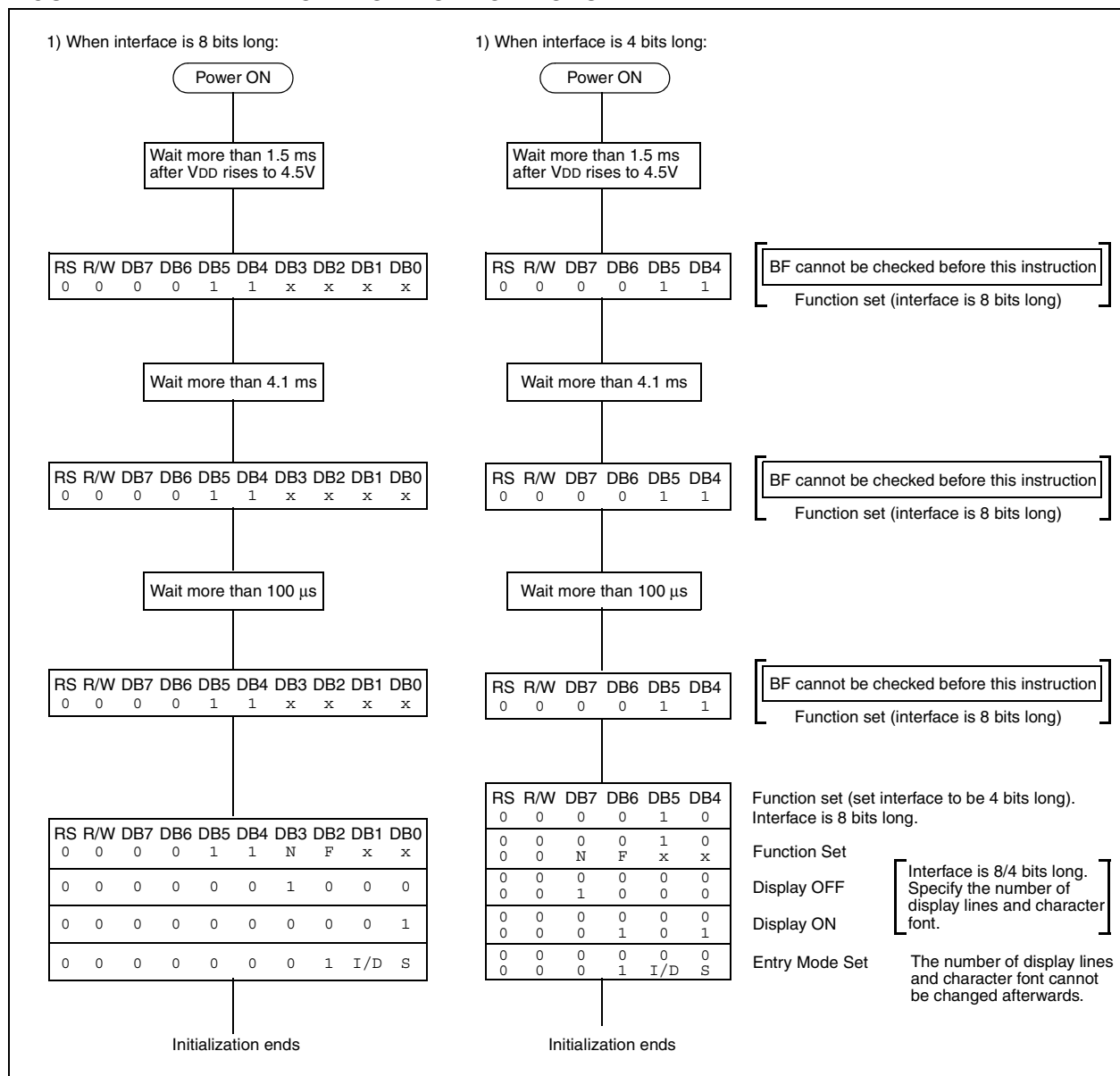
When the module powers up, the default data transfer mode is 8-bit. The initialization sequence only requires commands that are 4-bit in length. The last initialization

command needs to specify the data transfer width (4-or 8-bit). Then a delay of 4.6 ms must be executed before the LCD module can be initialized. Some of the LCD module commands are:

- 1 or 2 lines of characters
- Display on /off
- Clear display
- Increment / do not increment character address pointer after each character
- Load character address pointer

The initialization flow for the module is shown in Figure 4.

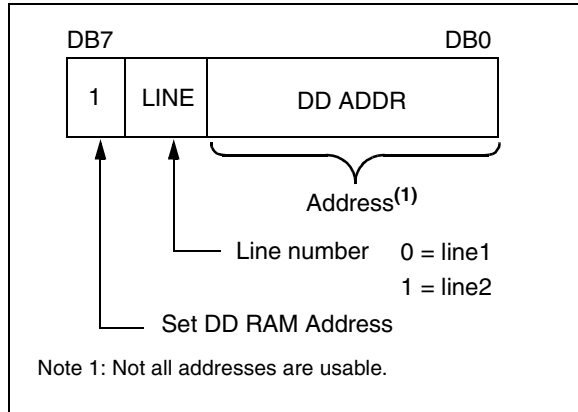
FIGURE 4: INITIALIZATION FLOW FOR LCD MODULE



AN587

After initialization, each character address is individually addressable. Figure 5 shows the structure of the command to specify the character address.

FIGURE 5: CHARACTER ADDRESS COMMAND FORMAT



The Hitachi Display Drive (HD44780A) has 80 bytes of RAM. The LM032L modules only use 40 bytes of the available RAM (2 x 20 characters). It is possible to use the remaining RAM locations for storage of other information.

Figure 6 shows the display data positions supported by the display driver as well as the characters actually displayed by the module (the non-shaded addresses).

The program example implemented here uses the character auto increment feature. This automatically increments the character address pointer after each character is written to the display.

CONCLUSION

The Hitachi LM032L character display module is well suited for displaying information. The selection of 4-bit or 8-bit data transfer mode is strictly a program memory size vs. I/O resource trade-off. The supplied code is easily used in any of three common data interfaces. The source is easily modifiable to a designers specific application needs. Other display modules/drivers maybe implemented with the appropriate modifications. Table 3 shows the resource requirements for the three subroutines SEND_CHAR, SEND_COMMAND, and BUSY_CHECK in the various data interface modes.

FIGURE 6: DISPLAY DRIVER (DD) RAM LOCATIONS

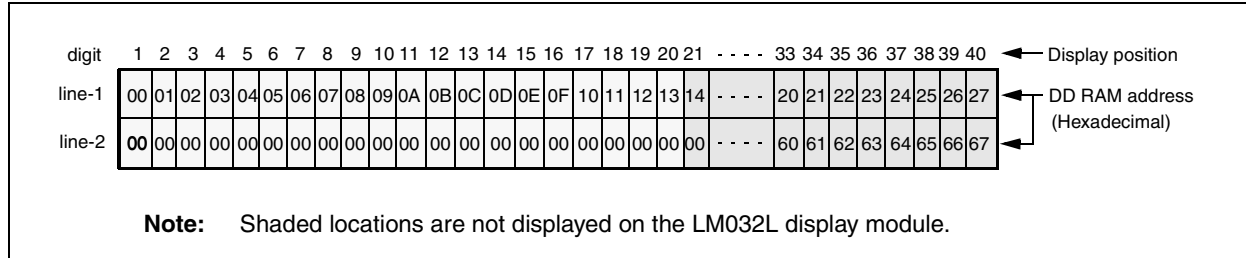


TABLE 3: RESOURCE REQUIREMENTS

Mode	Program Memory	Data Memory	Verified On
8-bit	32	3	PICDEM-2 ⁽¹⁾
4-bit, Data transferred on the high nibble of the port.	53	3	PICDEM-2 ⁽¹⁾
4-bit, Data transferred on the high nibble of the port.	53	3	Low-Power Real-Time Clock Board (AN582)

Note 1: Jumper J6 must be removed.

APPENDIX A: LM032L TIMING REQUIREMENTS

TABLE A-1: TIMING CHARACTERISTICS

Parameter #	Symbol	Characteristics	Min.	Typ.	Max.	Unit
1	TCYC	Enable cycle time	1.0	—	—	μs
2	PWEH	Enable pulse width	450	—	—	μs
3	TER, TEF	Enable rise / fall time	—	—	25	μs
4	TAS	RS, R/W set-up time	140	—	—	μs
5	TDDR	Data delay time	—	—	320	μs
6	Tdsu	Data setup time	195	—	—	μs
7	TH	Hold time	20	—	—	μs

FIGURE A-1: DATA WRITE INTERFACE TIMING

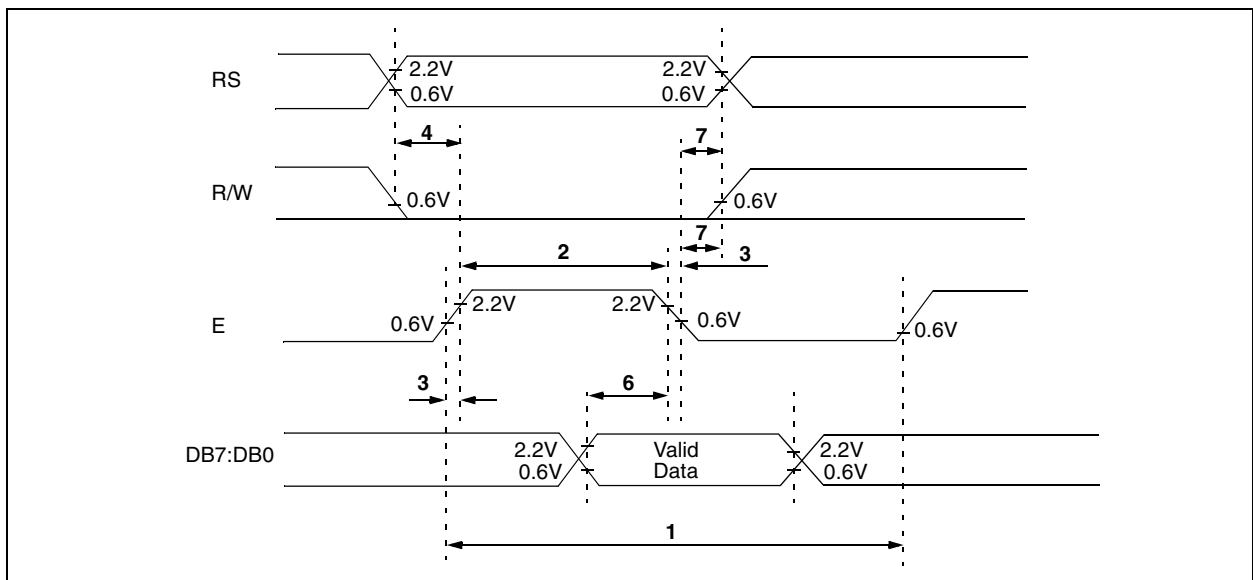
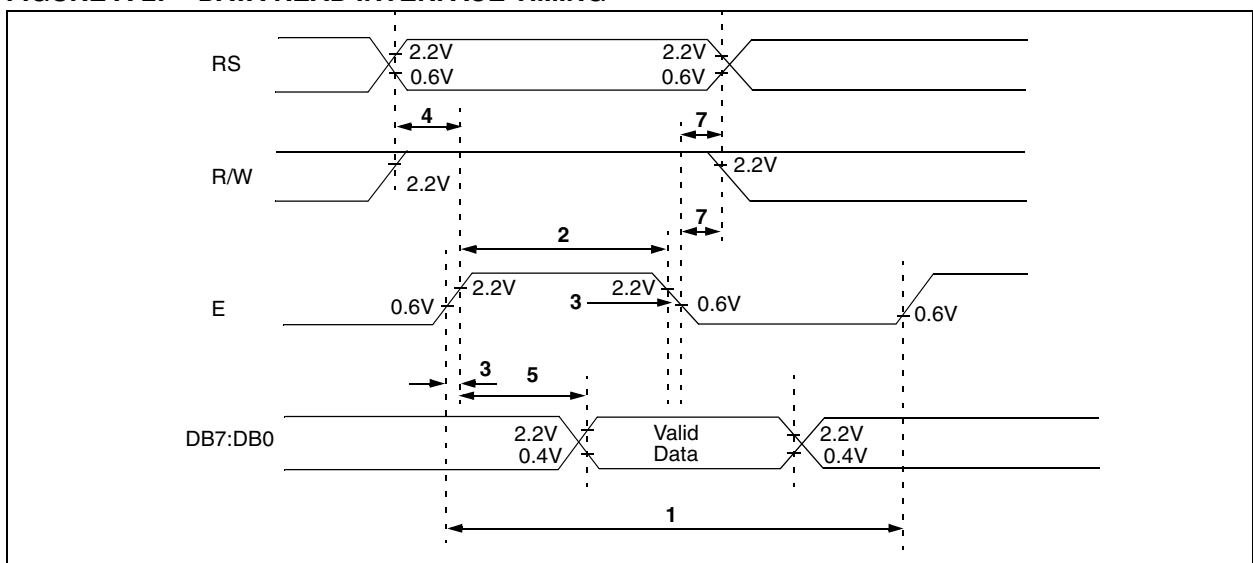


FIGURE A-2: DATA READ INTERFACE TIMING



Note: Refer to Hitachi documentation for the most current timing specifications.

AN587

TABLE A-2: LM032L PIN CONNECTION

Pin No.	Symbol	Level	Function	
1	VSS	—	0V	Ground
2	VDD	—	+5V	Power Supply(+)
3	Vo	—	—	Ground
4	RS	H/L	L: Instruction Code Input H: Data Input	
5	R/W	H/L	H: Data Read (LCD module→MPU) L: Data Write (LCD module←MPU)	
6	E	H,H→L	Enable Signal	
7	DB0	H/L	Data Bus Line Note (1), (2)	
8	DB1	H/L		
9	DB2	H/L		
10	DB3	H/L		
11	DB4	H/L		
12	DB5	H/L		
13	DB6	H/L		
14	DB7	H/L		

In the HD44780, the data can be sent in either two 4-bit operations or one 8-bit operation, This flexibility allows an interface to both 4- and 8-bit MPUs.

- Note 1: When interface data is 4-bits long, data is transferred using only 4 lines of DB7:DB4 (DB3:DB0 are not used). Data transfer between the HD44780 and the MPU completes when 4-bits of data is transferred twice. Data of the higher order 4 bits (contents of DB7:DB4 when interface data is 8-bits long) is transferred first and then lower order 4 bits (contents of DB3:DB0 when interface data is 8-bits long).
- 2: When interface data is 8-bits long, data is transferred using 8 data lines of DB7:DB0.

Please check the Microchip BBS for the latest version of the source code. Microchip's Worldwide Web Address: www.microchip.com; Bulletin Board Support: MCHIPBBS using CompuServe® (CompuServe membership not required).

APPENDIX B: 8-BIT DATA INTERFACE LISTING

MPASM 01.40.01 Intermediate LM032L.ASM 4-7-1997 9:43:02 PAGE 1

```

LOC OBJECT CODE      LINE SOURCE TEXT
VALUE
00001          LIST P=16C64
00002          ERRORLEVEL -302
00003 ;
00004 ; This program interfaces to a Hitachi (LM032L) 2 line by 20 character display
00005 ; module. The program assembles for either 4-bit or 8-bit data interface, depending
00006 ; on the value of the 4bit flag. LCD_DATA is the port which supplies the data to
00007 ; the LM032L, while LCD_CNTL is the port that has the control lines ( E, RS, RW ).
00008 ; In 4-bit mode the data is transfer on the high nibble of the port ( PORT<7:4> ).
00009 ;
00010 ; Program = LM032L.ASM
00011 ; Revision Date: 5-10-94
00012 ; 1-22-97 Compatibility with MPASMWIN 1.40
00013 ;
00014 ;
00015 ; include <pl6c64.inc>
00016 ; LIST
00017 ; P16C64.INC Standard Header File, Version 1.01 Microchip Technology, Inc.
00018 ; LIST
00019 EQU 9F
00020 EQU 0
00021 EQU 1
00022 include <lm032l.h>
00023 list
00024 Four_bit EQU TRUE
00025 Data_HI EQU FALSE
00026 ;
00027 ;
00028 if ( Four_bit && !Data_HI )
00029 ;
00030 LCD_DATA EQU PORTB
00031 LCD_DATA_TRIS EQU TRISB
; Selects 4- or 8-bit data transfers
; If 4-bit transfers, Hi or Low nibble of PORT

```

```

00032 ;
00033     else
00034 ;
00035 LCD_DATA EQU PORTD
00036 LCD_DATA_TRIS EQU TRISD
00037 ;
00038     endif
00039 ;
00040 LCD_CNTRL EQU PORTA
00041 ;
00042 ;
00043 ;
00044 ; LCD Display Commands and Control Signal names.
00045 ;
00046     if ( Four_bit && !Data_HI )
00047 ;
00048 E EQU 0 ; LCD Enable control line
00049 RW EQU 1 ; LCD Read/Write control line
00050 RS EQU 2 ; LCD Register Select control line
00051 ;
00052     else
00053 ;
00054 E EQU 3 ; LCD Enable control line
00055 RW EQU 2 ; LCD Read/Write control line
00056 RS EQU 1 ; LCD Register Select control line
00057 ;
00058     endif
00059 ;
00060 ;
00061 TEMP1 EQU 0x030
00062 ;
00063     org RESET_V ; RESET vector location
00064 RESET GOTO START ;
00065 ;
00066 ; This is the Peripheral Interrupt routine. Should NOT get here
00067 ;
00068     page
00069     org ISR_V ; Interrupt vector location
00070 PER_INT_V
00071 ERROR1 BCF STATUS, RP0 ; Bank 0
00072         BSF PORTC, 0
00073         BCF PORTC, 0
00074         GOTO ERROR1
00075 ;
00076 ;
00077 ;
00078 START ; POWER_ON Reset (Beginning of program)
0008

```



```

0008 0183          CLRFS  STATUS      ; Do initialization (Bank 0)
0009 0184          CLRFS  INTCON
000A 0185          CLRFS  PIR1
000B 1683          BSF   STATUS, RP0      ; Bank 1
000C 3000          MOVLW 0x00           ; The LCD module does not like to work w/ weak pull-ups
000D 0081          MOVWF  OPTION_REG    ;
000E 018C          CLRFS  PIE1           ; Disable all peripheral interrupts
;***
00086 ;***
00087 ;***      If using device with A/D, these two instructions are required.
00088 ;***
00089 ;          MOVLW 0xFF
00090 ;          MOVWF  ADCON1
;
00091 ;
00092 ;
00093 ;          BSF   STATUS, RP0      ; Bank 0
00094 ;          CLRFS  PORTA          ; ALL PORT output should output Low.
00095 ;          CLRFS  PORTB
00096 ;          CLRFS  PORTC
00097 ;          CLRFS  PORTD
00098 ;          CLRFS  PORTE
00099 ;          BCF   TICON, TMR1ON    ; Timer 1 is NOT incrementing
00100 ;
00101 ;          BSF   STATUS, RP0      ; Select Bank 1
00102 ;          CLRFS  TRISA          ; RA5 - 0 outputs
00103 ;          MOVLW 0xF0
00104 ;          MOVWF  TRISB          ; RB7 - 4 inputs, RB3 - 0 outputs
00105 ;          CLRFS  TRISC          ; RC Port are outputs
00106 ;          BSF   TRISC, T1OSO    ; RC0 needs to be input for the oscillator to function
00107 ;          CLRFS  TRISD          ; RD Port are outputs
00108 ;          CLRFS  TRISE          ; RE Port are outputs
00109 ;          BSF   PIE1, TMR1IE    ; Enable TMR1 Interrupt
00110 ;          BSF   OPTION_REG,NOT_RBPU ; Disable PORTB pull-ups
00111 ;          BCF   STATUS, RP0      ; Select Bank 0
00112 ;
00113 ;          page
00114 ;
00115 ; Initialize the LCD Display Module
00116 ;
00117 ;          CLRFS  LCD_CNTRL
00118 ;
00119 DISPLAY_INIT
00120   if ( Four_bit && !Data_HI )
00121     MOVLW 0x02
00122   endif
00123 ;
00124   if ( Four_bit && Data_HI )
00125     MOVLW 0x020

```

```

00126     endif
00127 ;
00128     if ( !Four_bit )
00129         MOVLW  0x038             ; Command for 8-bit interface
00130     endif
00131 ;
00132         MOVWF  LCD_DATA         ;
00133         BSF    LCD_CNTRL, E     ;
00134         BCF    LCD_CNTRL, E     ;
00135 ;
00136 ; This routine takes the calculated times that the delay loop needs to
00137 ; be executed, based on the LCD_INIT_DELAY EQUate that includes the
00138 ; frequency of operation. These uses registers before they are needed to
00139 ; store the time.
00140 ;
00141 LCD_DELAY  MOVLW  LCD_INIT_DELAY ;
00142             MOVWF  MSD           ; Use MSD and LSD Registers to Initialize LCD
00143             CLRF  LSD           ;
00144 LOOP2     DEFSZ  LSD, F         ; Delay time = MSD * ((3 * 256) + 3) * Tcy
00145             GOTO  LOOP2        ;
00146             DEFSZ  MSD, F       ;
00147 END_LCD_DELAY
00148             GOTO  LOOP2        ;
00149 ;
00150 ; Command sequence for 2 lines of 5x7 characters
00151 ;
00152 CMD_SEQ   MOVLW  0x020         ; 4-bit low nibble xfer
00153 ;
00154     if ( Four_bit )
00155         if ( !Data_HI )
00156             MOVLW  0x020         ; 4-bit high nibble xfer
00157         else
00158             MOVLW  0x020         ; 8-bit mode
00159         endif
00160 ;
00161     else
00162         MOVLW  0x038
00163     endif
00164 ;
00165         MOVWF  LCD_DATA         ; This code for both 4-bit and 8-bit modes
00166         BSF    LCD_CNTRL, E     ;
00167         BCF    LCD_CNTRL, E     ;
00168 ;
00169     if ( Four_bit )
00170         if ( !Data_HI )
00171             MOVLW  0x08         ; 4-bit low nibble xfer
00172         else

```

```

00173          MOVWL 0x080          ; 4-bit high nibble xfer
00174      endif
00175      MOVWF LCD_DATA          ;
00176      BSF LCD_CNTRL, E        ;
00177      BCF LCD_CNTRL, E        ;
00178      endif
00179 ;
00180 ; Busy Flag should be valid after this point
00181 ;
00182      MOVWL DISP_ON          ;
00183      CALL SEND_CMD          ;
00184      MOVWL CLR_DISP          ;
00185      CALL SEND_CMD          ;
00186      MOVWL ENTRY_INC        ;
00187      CALL SEND_CMD          ;
00188      MOVWL DD_RAM_ADDR       ;
00189      CALL SEND_CMD          ;
00190 ;
00191      page
00192 ;
00193 ;Send a message the hard way
00194      movlw 'M'
00195      call SEND_CHAR
00196      movlw 'i'
00197      call SEND_CHAR
00198      movlw 'c'
00199      call SEND_CHAR
00200      movlw 'r'
00201      call SEND_CHAR
00202      movlw 'o'
00203      call SEND_CHAR
00204      movlw 'c'
00205      call SEND_CHAR
00206      movlw 'h'
00207      call SEND_CHAR
00208      movlw 'i'
00209      call SEND_CHAR
00210      movlw 'p'
00211      call SEND_CHAR
00212
00213      movlw B'11000000'
00214      call SEND_CMD
00215
00216
00217      movlw 0
00218      dispsmsg
00219      movwf TEMP1
;Address DDrAm first character, second line
;Demonstration of the use of a table to output a message
;Table address of start of message
;TEMP1 holds start of message address

```

```

0053 2099      call    Table
0054 39FF      andlw  0FFh
0055 1903      btfscc STATUS,Z
0056 285B      goto   out
0057 2063      call   SEND_CHAR
0058 0830      movf  TEMP1,w
0059 3E01      addlw  1
005A 2852      goto  dispmsg
005B
005B 285B      goto  loop
005C
005C 300C      MOVLW DISP_ON
005D 2072      CALL  SEND_CMD
005E 3001      MOVLW CLR_DISP
005F 2072      CALL  SEND_CMD
0060 3006      MOVLW ENTRY_INC
0061 2072      CALL  SEND_CMD
0062 0008      RETURN

00241 ;
00242      page
00243 ;
00244 ;*****
00245 ;* The LCD Module Subroutines
00246 ;*****
00247 ;
00248      if ( Four_bit )      ; 4-bit Data transfers?
00249 ;
00250      if ( Data_HI )      ; 4-bit transfers on the high nibble of the PORT
00251 ;
00252 ;*****
00253 ;*SendChar - Sends character to LCD
00254 ;*This routine splits the character into the upper and lower
00255 ;*nibbles and sends them to the LCD, upper nibble first.
00256 ;*****
00257 ;
00258 SEND_CHAR
00259      MOVWF CHAR
00260      CALL  BUSY_CHECK
00261      MOVF  CHAR, w
00262      ANDLW 0xF0
00263      MOVWF LCD_DATA
00264      BCF   LCD_CNTRL, RW
00265      BSF   LCD_CNTRL, RS
00266      BSF   LCD_CNTRL, E

;Check if at end of message (zero
;returned at end)
;Display character
;Point to next character
;Stay here forever
; Display On, Cursor On
; Send This command to the Display Module
; Clear the Display
; Send This command to the Display Module
; Set Entry Mode Inc., No shift
; Send This command to the Display Module
; Character to be sent is in W
;Wait for LCD to be ready
;Get upper nibble
;Send data to LCD
;Set LCD to read
;Set LCD to data mode
;toggle E for LCD

```

```

00267         BCF     LCD_CNTL, E
00268     SWAPF   CHAR, w
00269     ANDLW   0xF0
00270     MOVWF   LCD_DATA
00271     BSF     LCD_CNTL, E
00272     BCF     LCD_CNTL, E
00273     RETURN
00274 ;
00275     else
00276 ;
00277 ;*****
00278 ;* SEND_CHAR - Sends character to LCD
00279 ;* This routine splits the character into the upper and lower
00280 ;* nibbles and sends them to the LCD, upper nibble first.
00281 ;* The data is transmitted on the PORT<3:0> pins
00282 ;*****
00283 ;
00284     SEND_CHAR
00285     MOVWF   CHAR
00286     CALL    BUSY_CHECK
00287     SWAPF   CHAR, w
00288     ANDLW   0x0F
00289     MOVWF   LCD_DATA
00290     BCF     LCD_CNTL, RW
00291     BSF     LCD_CNTL, RS
00292     BSF     LCD_CNTL, E
00293     BCF     LCD_CNTL, E
00294     MOVF    CHAR, w
00295     ANDLW   0x0F
00296     MOVWF   LCD_DATA
00297     BSF     LCD_CNTL, E
00298     BCF     LCD_CNTL, E
00299     RETURN
00300 ;
00301     endif
00302     else
00303 ;
00304 ;*****
00305 ;* SEND_CHAR - Sends character contained in register W to LCD
00306 ;* This routine sends the entire character to the PORT
00307 ;* The data is transmitted on the PORT<7:0> pins
00308 ;*****
00309 ;
00310     SEND_CHAR
00311     MOVWF   CHAR
00312     CALL    BUSY_CHECK
00313     MOVF    CHAR, w

```

```

00314 MOVWF LCD_DATA ; Send data to LCD
00315 BCF LCD_CNTRL, RW ; Set LCD in read mode
00316 BSF LCD_CNTRL, RS ; Set LCD in data mode
00317 BSF LCD_CNTRL, E ; toggle E for LCD
00318 BCF LCD_CNTRL, E
00319 RETURN
00320 ;
00321 endif
00322 ;
00323 page
00324 ;
00325 ;*****
00326 ;* SendCmd - Sends command to LCD *
00327 ;* This routine splits the command into the upper and lower *
00328 ;* nibbles and sends them to the LCD, upper nibble first. *
00329 ;* The data is transmitted on the PORT<3:0> pins *
00330 ;*****
00331 ;
00332 if ( Four_bit ) ; 4-bit Data transfers?
00333 ;
00334 if ( Data_HI ) ; 4-bit transfers on the high nibble of the PORT
00335 ;
00336 ;*****
00337 ;* SEND_CMD - Sends command to LCD *
00338 ;* This routine splits the command into the upper and lower *
00339 ;* nibbles and sends them to the LCD, upper nibble first. *
00340 ;*****
00341
00342 SEND_CMD
00343 MOVWF CHAR ; Character to be sent is in W
00344 CALL BUSY_CHECK ; Wait for LCD to be ready
00345 MOVF CHAR, w
00346 ANDLW 0xF0 ; Get upper nibble
00347 MOVWF LCD_DATA ; Send data to LCD
00348 BCF LCD_CNTRL, RW ; Set LCD to read
00349 BCF LCD_CNTRL, RS ; Set LCD to command mode
00350 BSF LCD_CNTRL, E ; toggle E for LCD
00351 BCF LCD_CNTRL, E
00352 SWAPF CHAR, w
00353 ANDLW 0xF0 ; Get lower nibble
00354 MOVWF LCD_DATA ; Send data to LCD
00355 BSF LCD_CNTRL, E ; toggle E for LCD
00356 BCF LCD_CNTRL, E
00357 RETURN
00358 ;
00359 else ; 4-bit transfers on the low nibble of the PORT
00360 ;

```

```

0072      0072 00B6
0073      0073 2081
0074      0074 0E36
0075      0075 390F
0076      0076 0086
0077      0077 1085
0078      0078 1105
0079      0079 1405
007A      007A 1005
007B      007B 0836
007C      007C 390F
007D      007D 0086
007E      007E 1405
007F      007F 1005
0080      0080 0008

00361 SEND_CMD
00362      MOVWF CHAR
00363      CALL BUSY_CHECK
00364      SWAPF CHAR, W
00365      ANDLW 0x0F
00366      MOVWF LCD_DATA
00367      BCF LCD_CNTRL, RW
00368      BCF LCD_CNTRL, RS
00369      BSF LCD_CNTRL, E
00370      BCF LCD_CNTRL, E
00371      MOVF CHAR, W
00372      ANDLW 0x0F
00373      MOVWF LCD_DATA
00374      BSF LCD_CNTRL, E
00375      BCF LCD_CNTRL, E
00376      RETURN
00377 ;
00378      endif
00379      else
00380 ;
00381 ;*****
00382 ;* SEND_CMD - Sends command contained in register W to LCD *
00383 ;* This routine sends the entire character to the PORT *
00384 ;* The data is transmitted on the PORT<7:0> pins *
00385 ;*****
00386

00387 SEND_CMD
00388      MOVWF CHAR
00389      CALL BUSY_CHECK
00390      MOVF CHAR, W
00391      MOVWF LCD_DATA
00392      BCF LCD_CNTRL, RW
00393      BCF LCD_CNTRL, RS
00394      BSF LCD_CNTRL, E
00395      BCF LCD_CNTRL, E
00396      RETURN
00397 ;
00398      endif
00399 ;
00400      page
00401 ;
00402 ; if ( Four_bit )
00403 ;
00404 ; if ( Data_HI )
00405 ;
00406 ;*****
00407 ;* This routine checks the busy flag, returns when not busy *

```

```

00408 ; * Affects:
00409 ; *   TEMP - Returned with busy/address
00410 ; *****
00411 ;
00412 BUSY_CHECK
00413     BSF     STATUS, RP0      ; Select Register Bank1
00414     MOVLW  0xFF           ; Set Port_D for input
00415     MOVWF  LCD_DATA_TRIS
00416     BCF     STATUS, RP0      ; Select Register Bank0
00417     BCF     LCD_CNTRL, RS    ; Set LCD for Command mode
00418     BSF     LCD_CNTRL, RW    ; Setup to read busy flag
00419     BSF     LCD_CNTRL, E     ; Set E high
00420     BCF     LCD_CNTRL, E     ; Set E low
00421     MOVF   LCD_DATA, W      ; Read upper nibble busy flag, DDRam address
00422     ANDLW  0xF0           ; Mask out lower nibble
00423     MOVWF  TEMP
00424     BSF     LCD_CNTRL, E     ; Toggle E to get lower nibble
00425     BCF     LCD_CNTRL, E
00426     SWAPF  LCD_DATA, W      ; Read lower nibble busy flag, DDRam address
00427     ANDLW  0x0F           ; Mask out upper nibble
00428     IORWF  TEMP           ; Combine nibbles
00429     BTFSC  TEMP, 7         ; Check busy flag, high = busy
00430     GOTO   BUSY_CHECK      ; If busy, check again
00431     BCF     LCD_CNTRL, RW    ; Select Register Bank1
00432     BSF     STATUS, RP0
00433     MOVLW  0x0F
00434     MOVWF  LCD_DATA_TRIS    ; Set Port_D for output
00435     BCF     STATUS, RP0      ; Select Register Bank0
00436     RETURN
00437 ;
00438     else
00439 ; *****
00440 ; *****
00441 ; * This routine checks the busy flag, returns when not busy
00442 ; * Affects:
00443 ; *   TEMP - Returned with busy/address
00444 ; *****
00445 ;
00446 BUSY_CHECK
00447     BSF     STATUS, RP0      ; Bank 1
00448     MOVLW  0xFF           ; Set PortB for input
00449     MOVWF  LCD_DATA_TRIS
00450     BCF     STATUS, RP0      ; Bank 0
00451     BCF     LCD_CNTRL, RS    ; Set LCD for Command mode
00452     BSF     LCD_CNTRL, RW    ; Setup to read busy flag
00453     BSF     LCD_CNTRL, E     ; Set E high
00454     BCF     LCD_CNTRL, E     ; Set E low
0081
0081 1683
0082 30FF
0083 0086
0084 1283
0085 1105
0086 1485
0087 1405
0088 1005

```



```

0089 0E06          LCD_DATA, W          ; Read upper nibble busy flag, DDRam address
008A 39F0          LCD_DATA, W          ; Mask out lower nibble
008B 00B5          MOVWF          TEMP
008C 1405          BSF          LCD_CNTRL, E          ; Toggle E to get lower nibble
008D 1005          BCF          LCD_CNTRL, E
008E 0806          MOVF          LCD_DATA, W
008F 390F          ANDLW         0x0F
0090 04B5          IORWF          TEMP, F
0091 1BB5          BTFSCL         TEMP, 7
0092 2881          GOTO          BUSY_CHECK
0093 1085          BCF          LCD_CNTRL, RW
0094 1683          BSF          STATUS, RP0
0095 30F0          MOVLW         0xF0
0096 0086          MOVWF         LCD_DATA_TRIS
0097 1283          BCF          STATUS, RP0
0098 0008          RETURN

00471 ;
00472          endif
00473          else
00474 ;
00475 ;*****
00476 ;* This routine checks the busy flag, returns when not busy *
00477 ;* Affects: *
00478 ;* TEMP - Returned with busy/address *
00479 ;*****
00480 ;
00481 BUSY_CHECK
00482          BSF          STATUS,RP0          ; Select Register Bank1
00483          MOVLW         0xFF          ; Set port_D for input
00484          MOVWF         LCD_DATA_TRIS
00485          BCF          STATUS, RP0          ; Select Register Bank0
00486          BCF          LCD_CNTRL, RS          ; Set LCD for command mode
00487          BSF          LCD_CNTRL, RW          ; Setup to read busy flag
00488          BSF          LCD_CNTRL, E          ; Set E high
00489          BCF          LCD_CNTRL, E          ; Set E low
00490          MOVF          LCD_DATA, w          ; Read busy flag, DDRam address
00491          MOVWF         TEMP
00492          BTFSCL         TEMP, 7          ; Check busy flag, high=busy
00493          GOTO          BUSY_CHECK
00494          BCF          LCD_CNTRL, RW
00495          BSF          STATUS, RP0          ; Select Register Bank1
00496          MOVLW         0x00
00497          MOVWF         LCD_DATA_TRIS
00498          BCF          STATUS, RP0          ; Set port_D for output
00499          RETURN
00500 ;
00501          endif

```

```

0099          00502      page
0099          00503      ;
0099          00504      Table
0099          00505          addwF    PCL, F
0099          00506          retlw   'M'
0099          00507          retlw   'i'
0099          00508          retlw   'c'
0099          00509          retlw   'r'
0099          00510          retlw   'o'
0099          00511          retlw   'c'
0099          00512          retlw   'h'
0099          00513          retlw   'i'
0099          00514          retlw   'p'
0099          00515          retlw   '\ '
0099          00516          retlw   'T'
0099          00517          retlw   'e'
0099          00518          retlw   'c'
0099          00519          retlw   'h'
0099          00520          retlw   'n'
0099          00521          retlw   'o'
0099          00522          retlw   'l'
0099          00523          retlw   'o'
0099          00524          retlw   'g'
0099          00525          retlw   'y'
0099          00526      Table_End
0099          00527          retlw   0
0099          00528      ;
0099          00529          if ( (Table & 0x0FF) >= (Table_End & 0x0FF) )
0099          00530              MESSG    "Warning - User Defined: Table crosses page boundary in computed jump"
0099          00531          endif
0099          00532      ;
0099          00533
0099          00534
0099          00535
0099          00536      end

MEMORY USAGE MAP ('X' = Used, '-' = Unused)

0000 : X--XXXXXXXXXXXX XXXXXXXXXXXXXXXXXXXX XXXXXXXXXXXXXXXXXXXX XXXXXXXXXXXXXXXXXXXX
0040 : XXXXXXXXXXXXXXXX XXXXXXXXXXXXXXXXXXXX XXXXXXXXXXXXXXXXXXXX XXXXXXXXXXXXXXXXXXXX
0080 : XXXXXXXXXXXXXXXX XXXXXXXXXXXXXXXXXXXX XXXXXXXXXXXXXXXXXXXX XXXXXXXXXXXXXXXXXXXX
-----

All other memory blocks unused.

Program Memory Words Used: 172
Program Memory Words Free: 1876

```

Errors : 0
Warnings : 0 reported, 0 suppressed
Messages : 0 reported, 12 suppressed

Please check the Microchip BBS for the latest version of the source code. Microchip's Worldwide Web Address: www.microchip.com; Bulletin Board Support: MCHIPBBS using CompuServe® (CompuServe membership not required).

APPENDIX C: 4-BIT DATA INTERFACE, HIGH NIBBLE LISTING

MPASM 01.40.01 Intermediate LM032L.ASM 4-7-1997 9:50:32 PAGE 1

```

LOC OBJECT CODE      LINE SOURCE TEXT
VALUE
00001          LIST P=16C64
00002          ERRORLEVEL -302
00003          ;
00004          ; This program interfaces to a Hitachi (LM032L) 2 line by 20 character display
00005          ; module. The program assembles for either 4-bit or 8-bit data interface, depending
00006          ; on the value of the 4bit flag. LCD_DATA is the port which supplies the data to
00007          ; the LM032L, while LCD_CNTL is the port that has the control lines ( E, RS, RW ).
00008          ; In 4-bit mode the data is transfer on the high nibble of the port ( PORT<7:4> ).
00009          ;
00010          ; Program = LM032L.ASM
00011          ; Revision Date: 5-10-94
00012          ; 1-22-97      Compatibility with MPASMWIN 1.40
00013          ;
00014          ;
00015          ; include <pl6c64.inc>
00016          ;
00017          ; LIST
00018          ; P16C64.INC Standard Header File, Version 1.01 Microchip Technology, Inc.
00019          ; LIST
00020          EQU 9F
00021          EQU 0
00022          EQU 1
00023          include <lm032l.h>
00024          list
00025          ;
00026          ;
00027          ;
00028          ; Four_bit EQU FALSE ; Selects 4- or 8-bit data transfers
00029          ; Data_HI EQU TRUE ; If 4-bit transfers, Hi or Low nibble of PORT
00030          ; if ( Four_bit && !Data_HI )
00031          ; LCD_DATA EQU PORTB
00032          ; LCD_DATA_TRIS EQU TRISB

```

```

00032 ;
00033     else
00034 ;
00035 LCD_DATA EQU PORTD
00036 LCD_DATA_TRIS EQU TRISD
00037 ;
00038     endif
00039 ;
00040 LCD_CNTL EQU PORTA
00041 ;
00042 ;
00043 ;
00044 ; LCD Display Commands and Control Signal names.
00045 ;
00046     if ( Four_bit && !Data_HI )
00047 ;
00048 E EQU 0 ; LCD Enable control line
00049 RW EQU 1 ; LCD Read/Write control line
00050 RS EQU 2 ; LCD Register Select control line
00051 ;
00052     else
00053 ;
00054 E EQU 3 ; LCD Enable control line
00055 RW EQU 2 ; LCD Read/Write control line
00056 RS EQU 1 ; LCD Register Select control line
00057 ;
00058     endif
00059 ;
00060 ;
00061 TEMP1 EQU 0x030
00062 ;
00063     org RESET_V ; RESET vector location
00064 RESET GOTO START ;
00065 ;
00066 ; This is the Peripheral Interrupt routine. Should NOT get here
00067 ;
00068     page
00069     org ISR_V ; Interrupt vector location
00070 PER_INT_V
00071 ERROR1 BCF STATUS, RP0 ; Bank 0
00072         BSF PORTC, 0
00073         BCF PORTC, 0
00074         GOTO ERROR1
00075 ;
00076 ;
00077 ;
00078 START ; POWER_ON Reset (Beginning of program)
0008

```

```

0008 0183          CLRFS  STATUS, RP0          ; Do initialization (Bank 0)
0009 0184          CLRFS  INTCON
000A 0185          CLRFS  PIR1
000B 0186          BSF    STATUS, RP0          ; Bank 1
000C 0187          MOVLW 0x00          ; The LCD module does not like to work w/ weak pull-ups
000D 0188          MOVWF  OPTION_REG        ;
000E 0189          CLRFS  PIE1            ; Disable all peripheral interrupts
00086 ;***
00087 ;***      If using device with A/D, these two instructions are required.
00088 ;***
00089 ;          MOVLW 0xFF
00090 ;          MOVWF  ADCON1            ; Port A is Digital.
00091 ;
00092 ;
00093          BCF    STATUS, RP0          ; Bank 0
00094          CLRFS  PORTA
00095          CLRFS  PORTB
00096          CLRFS  PORTC
00097          CLRFS  PORTD
00098          CLRFS  PORTE
00099          BCF    TICON, TMR1ON        ; Timer 1 is NOT incrementing
00100 ;
00101          BSF    STATUS, RP0          ; Select Bank 1
00102          CLRFS  TRISA
00103          MOVLW 0xF0
00104          MOVWF  TRISB
00105          CLRFS  TRISC
00106          BSF    TRISC, T1OSO        ; RC Port are outputs
00107          CLRFS  TRISD
00108          CLRFS  TRISE
00109          BSF    PIE1, TMR1IE        ; RC0 needs to be input for the oscillator to function
00110          BSF    OPTION_REG, NOT_RBPU ; RD Port are outputs
00111          BCF    STATUS, RP0          ; RE Port are outputs
00112 ;          ; Enable TMR1 Interrupt
00113          ;          ; Disable PORTB pull-ups
00114          ;          ; Select Bank 0
00115 ;          ;
00116 ;          ;
00117          CLRFS  LCD_CNTRL            ; ALL PORT output should output Low.
00118
00119 DISPLAY_INIT
00120   if ( Four_bit && !Data_HI )
00121     MOVLW 0x02
00122   endif
00123 ;
00124   if ( Four_bit && Data_HI )
00125     MOVLW 0x020

```

```

00126      endif
00127 ;
00128      if ( !Four_bit )
00129          MOVLW  0x038      ; Command for 8-bit interface
00130      endif
00131 ;
00132          MOVWF  LCD_DATA      ;
00133          BSF   LCD_CNTRL, E      ;
00134          BCF   LCD_CNTRL, E      ;
00135 ;
00136 ; This routine takes the calculated times that the delay loop needs to
00137 ; be executed, based on the LCD_INIT_DELAY EQUate that includes the
00138 ; frequency of operation. These use registers before they are needed to
00139 ; store the time.
00140 ;
00141 LCD_DELAY  MOVLW  LCD_INIT_DELAY      ;
00142          MOVWF  MSD      ; Use MSD and LSD Registers to Initialize LCD
00143          CLRF  LSD      ;
00144 LOOP2     DEFSZ  LSD, F      ; Delay time = MSD * ((3 * 256) + 3) * Tcy
00145          GOTO  LOOP2      ;
00146          DEFSZ  MSD, F      ;
00147          END_LCD_DELAY      ;
00148          GOTO  LOOP2      ;
00149 ;
00150 ; Command sequence for 2 lines of 5x7 characters
00151 ;
00152 CMD_SEQ
00153 ;
00154      if ( Four_bit )
00155          if ( !Data_HI )
00156              MOVLW  0x02      ; 4-bit low nibble xfer
00157          else
00158              MOVLW  0x020      ; 4-bit high nibble xfer
00159          endif
00160 ;
00161      else
00162          MOVLW  0x038      ; 8-bit mode
00163      endif
00164 ;
00165          MOVWF  LCD_DATA      ; This code for both 4-bit and 8-bit modes
00166          BSF   LCD_CNTRL, E      ;
00167          BCF   LCD_CNTRL, E      ;
00168 ;
00169          if ( Four_bit )
00170              if ( !Data_HI )
00171                  MOVLW  0x08      ; This code for only 4-bit mode (2nd xfer)
00172              else

```

```

00173          MOVWL 0x080          ; 4-bit high nibble xfer
00174      endif
00175      MOVWF LCD_DATA          ;
00176      BSF LCD_CNTRL, E        ;
00177      BCF LCD_CNTRL, E        ;
00178      endif
00179 ;
00180 ; Busy Flag should be valid after this point
00181 ;
00182      MOVWL DISP_ON          ;
00183      CALL SEND_CMD          ;
00184      MOVWL CLR_DISP          ;
00185      CALL SEND_CMD          ;
00186      MOVWL ENTRY_INC          ;
00187      CALL SEND_CMD          ;
00188      MOVWL DD_RAM_ADDR          ;
00189      CALL SEND_CMD          ;
00190 ;
00191      page
00192 ;
00193 ;Send a message the hard way
00194      movlw 'M'
00195      call SEND_CHAR
00196      movlw 'i'
00197      call SEND_CHAR
00198      movlw 'c'
00199      call SEND_CHAR
00200      movlw 'r'
00201      call SEND_CHAR
00202      movlw 'o'
00203      call SEND_CHAR
00204      movlw 'c'
00205      call SEND_CHAR
00206      movlw 'h'
00207      call SEND_CHAR
00208      movlw 'i'
00209      call SEND_CHAR
00210      movlw 'p'
00211      call SEND_CHAR
00212
00213      movlw B'11000000'
00214      call SEND_CMD
00215
00216
00217      movlw 0
00218      dispmsg
00219      movwf TEMP1

```

;Address DDRam first character, second line
;Demonstration of the use of a table to output a message
;Table address of start of message
;TEMP1 holds start of message address


```

004F 2083      call    Table
0050 39FF      andlw  0FFh
0051 1903      btfsc  STATUS,Z
0052 2857      goto   out
0053 205F      call   SEND_CHAR
0054 0830      movf  TEMP1,w
0055 3E01      addlw 1
0056 284E      goto  dispsmsg
0057
0057 2857      goto  loop
0058
0058 300C      MOVLW DISP_ON
0059 2068      CALL  SEND_CMD
005A 3001      MOVLW CLR_DISP
005B 2068      CALL  SEND_CMD
005C 3006      MOVLW ENTRY_INC
005D 2068      CALL  SEND_CMD
005E 0008      RETURN
00241 ;
00242      page
00243 ;
00244 ;*****
00245 ; * The LCD Module Subroutines
00246 ;*****
00247 ;
00248      if ( Four_bit )      ; 4-bit Data transfers?
00249 ;
00250      if ( Data_HI )      ; 4-bit transfers on the high nibble of the PORT
00251 ;
00252 ;*****
00253 ;*SendChar - Sends character to LCD
00254 ;*This routine splits the character into the upper and lower
00255 ;*nibbles and sends them to the LCD, upper nibble first.
00256 ;*****
00257 ;
00258 SEND_CHAR      MOVWF  CHAR
00259              CALL  BUSY_CHECK
00260              MOVF  CHAR, w
00261              ANDLW 0xF0
00262              MOVWF LCD_DATA
00263              BCF  LCD_CNTRL, RW
00264              BSF  LCD_CNTRL, RS
00265              BSF  LCD_CNTRL, E
00266
;Check if at end of message (zero
;returned at end)
;Display character
;Point to next character
;Stay here forever
; Display On, Cursor On
; Send This command to the Display Module
; Clear the Display
; Send This command to the Display Module
; Set Entry Mode Inc., No shift
; Send This command to the Display Module
;Character to be sent is in W
;Wait for LCD to be ready
;Get upper nibble
;Send data to LCD
;Set LCD to read
;Set LCD to data mode
;toggle E for LCD

```

```

00267         LCD_CNTL, E
00268     SWAPF    CHAR, w
00269     ANDLW   0xF0
00270     MOVWF   LCD_DATA
00271     BSF     LCD_CNTL, E
00272     BCF     LCD_CNTL, E
00273     RETURN
00274 ;
00275     else
00276 ;
00277 ;*****
00278 ; SEND_CHAR - Sends character to LCD
00279 ; * This routine splits the character into the upper and lower
00280 ; * nibbles and sends them to the LCD, upper nibble first.
00281 ; * The data is transmitted on the PORT<3:0> pins
00282 ;*****
00283 ;
00284     SEND_CHAR
00285     MOVWF   CHAR
00286     CALL    BUSY_CHECK
00287     SWAPF   CHAR, w
00288     ANDLW   0x0F
00289     MOVWF   LCD_DATA
00290     BCF     LCD_CNTL, RW
00291     BSF     LCD_CNTL, RS
00292     BSF     LCD_CNTL, E
00293     BCF     LCD_CNTL, E
00294     MOVF    CHAR, w
00295     ANDLW   0x0F
00296     MOVWF   LCD_DATA
00297     BSF     LCD_CNTL, E
00298     BCF     LCD_CNTL, E
00299     RETURN
00300 ;
00301     endif
00302     else
00303 ;
00304 ;*****
00305 ; SEND_CHAR - Sends character contained in register W to LCD
00306 ; * This routine sends the entire character to the PORT
00307 ; * The data is transmitted on the PORT<7:0> pins
00308 ;*****
00309 ;
00310     SEND_CHAR
00311     MOVWF   CHAR
00312     CALL    BUSY_CHECK
00313     MOVF    CHAR, w
005F 00B6
005F 00B6
0060 2071
0061 0836

```

```

0062 0088      MOVWF LCD_DATA      ; Send data to LCD
0063 1105      BCF LCD_CNTRL, RW   ; Set LCD in read mode
0064 1485      BSF LCD_CNTRL, RS   ; Set LCD in data mode
0065 1585      BSF LCD_CNTRL, E   ; toggle E for LCD
0066 1185      BCF LCD_CNTRL, E
0067 0008      RETURN

00320 ;
00321      endif
00322 ;
00323      page
00324 ;
00325 ;*****
00326 ;* SendCmd - Sends command to LCD *
00327 ;* This routine splits the command into the upper and lower *
00328 ;* nibbles and sends them to the LCD, upper nibble first. *
00329 ;* The data is transmitted on the PORT<3:0> pins *
00330 ;*****
00331 ;
00332      if ( Four_bit )
00333 ;
00334          if ( Data_HI )
00335 ;
00336 ;*****
00337 ;* SEND_CMD - Sends command to LCD *
00338 ;* This routine splits the command into the upper and lower *
00339 ;* nibbles and sends them to the LCD, upper nibble first. *
00340 ;*****
00341
00342 SEND_CMD
00343      MOVWF CHAR
00344      CALL BUSY_CHECK
00345      MOVF CHAR,w
00346      ANDLW 0xF0
00347      MOVWF LCD_DATA
00348      BCF LCD_CNTRL,RW
00349      BCF LCD_CNTRL,RS
00350      BSF LCD_CNTRL,E
00351      BCF LCD_CNTRL,E
00352      SWAPF CHAR,w
00353      ANDLW 0xF0
00354      MOVWF LCD_DATA
00355      BSF LCD_CNTRL,E
00356      BCF LCD_CNTRL,E
00357      RETURN
00358 ;
00359      else
00360 ;

```

```

00361 SEND_CMD
00362 MOVWF CHAR
00363 CALL BUSY_CHECK
00364 SWAPF CHAR, W
00365 ANDLW 0x0F
00366 MOVWF LCD_DATA
00367 BCF LCD_CNTRL, RW
00368 BCF LCD_CNTRL, RS
00369 BSF LCD_CNTRL, E
00370 BCF LCD_CNTRL, E
00371 MOVF CHAR, W
00372 ANDLW 0x0F
00373 MOVWF LCD_DATA
00374 BSF LCD_CNTRL, E
00375 BCF LCD_CNTRL, E
00376 RETURN
00377 ;
00378 endif
00379 else
00380 ;
00381 ;*****
00382 ;* SEND_CMD - Sends command contained in register W to LCD *
00383 ;* This routine sends the entire character to the PORT *
00384 ;* The data is transmitted on the PORT<7:0> pins *
00385 ;*****
00386
00387 SEND_CMD
00388 MOVWF CHAR
00389 CALL BUSY_CHECK
00390 MOVF CHAR, W
00391 MOVWF LCD_DATA
00392 BCF LCD_CNTRL, RW
00393 BCF LCD_CNTRL, RS
00394 BSF LCD_CNTRL, E
00395 BCF LCD_CNTRL, E
00396 RETURN
00397 ;
00398 endif
00399 ;
00400 page
00401 ;
00402 if ( Four_bit )
00403 ;
00404 if ( Data_HI )
00405 ;
00406 ;*****
00407 ;* This routine checks the busy flag, returns when not busy *
; Character to be sent is in W
; Wait for LCD to be ready
; Get upper nibble
; Send data to LCD
; Set LCD to read
; Set LCD to command mode
; toggle E for LCD
; Get lower nibble
; Send data to LCD
; toggle E for LCD
; Command to be sent is in W
; Wait for LCD to be ready
; Send data to LCD
; Set LCD in read mode
; Set LCD in command mode
; toggle E for LCD
; 4-bit Data transfers?
; 4-bit transfers on the high nibble of the PORT
;*****
; This routine checks the busy flag, returns when not busy *

```

```

00408 ; * Affects: *
00409 ; * TEMP - Returned with busy/address *
00410 ; *****
00411 ;
00412 BUSY_CHECK
00413 BSF STATUS, RP0 ; Select Register Bank1
00414 MOVLW 0xFF ; Set Port_D for input
00415 MOVWF LCD_DATA_TRIS
00416 BCF STATUS, RP0 ; Select Register Bank0
00417 BCF LCD_CNTRL, RS ; Set LCD for Command mode
00418 BSF LCD_CNTRL, RW ; Setup to read busy flag
00419 BCF LCD_CNTRL, E ; Set E high
00420 MOVF LCD_CNTRL, E ; Set E low
00421 ANDLW LCD_DATA, W ; Read upper nibble busy flag, DDRam address
00422 MOVWF TEMP ; Mask out lower nibble
00423 BSF LCD_CNTRL, E ; Toggle E to get lower nibble
00424 BCF LCD_CNTRL, E ; Read lower nibble busy flag, DDRam address
00425 SWAPF LCD_DATA, W ; Mask out upper nibble
00426 ANDLW 0x0F ; Combine nibbles
00427 IORWF TEMP, F ; Check busy flag, high = busy
00428 BTFSC TEMP, 7 ; If busy, check again
00429 GOTO BUSY_CHECK
00430 BCF LCD_CNTRL, RW ; Select Register Bank1
00431 BSF STATUS, RP0 ; Set Port_D for output
00432 MOVLW 0x0F ; Select Register Bank0
00433 MOVWF LCD_DATA_TRIS
00434 BCF STATUS, RP0
00435 RETURN
00436
00437 ;
00438 ; else ; 4-bit transfers on the low nibble of the PORT
00439 ;
00440 ; *****
00441 ; * This routine checks the busy flag, returns when not busy *
00442 ; * Affects: *
00443 ; * TEMP - Returned with busy/address *
00444 ; *****
00445 ;
00446 BUSY_CHECK
00447 BSF STATUS, RP0 ; Bank 1
00448 MOVLW 0xFF ; Set PortB for input
00449 MOVWF LCD_DATA_TRIS
00450 BCF STATUS, RP0 ; Bank 0
00451 BCF LCD_CNTRL, RS ; Set LCD for Command mode
00452 BSF LCD_CNTRL, RW ; Setup to read busy flag
00453 BCF LCD_CNTRL, E ; Set E high
00454 BCF LCD_CNTRL, E ; Set E low

```

```

00455 SWAPF LCD_DATA, W ; Read upper nibble busy flag, DDRam address
00456 ANDLW 0xFF ; Mask out lower nibble
00457 MOVWF TEMP ;
00458 BCF LCD_CNTRL, E ; Toggle E to get lower nibble
00459 BCF LCD_CNTRL, E ;
00460 MOVF LCD_DATA, W ; Read lower nibble busy flag, DDRam address
00461 ANDLW 0x0F ; Mask out upper nibble
00462 IORWF TEMP, F ; Combine nibbles
00463 BTFSF TEMP, 7 ; Check busy flag, high = busy
00464 GOTO BUSY_CHECK ; If busy, check again
00465 BCF LCD_CNTRL, RW ;
00466 BSF STATUS, RP0 ; Bank 1
00467 MOVLW 0xFF ;
00468 MOVWF LCD_DATA_TRIS ; RB7 - 4 = inputs, RB3 - 0 = output
00469 BCF STATUS, RP0 ; Bank 0
00470 RETURN
00471 ;
00472 endif
00473 else
00474 ;
00475 ;*****
00476 ;* This routine checks the busy flag, returns when not busy *
00477 ;* Affects: *
00478 ;* TEMP - Returned with busy/address *
00479 ;*****
00480 ;
00481 BUSY_CHECK
00482 BSF STATUS,RP0 ; Select Register Bank1
00483 MOVLW 0xFF ; Set port_D for input
00484 MOVWF LCD_DATA_TRIS
00485 BCF STATUS, RP0 ; Select Register Bank0
00486 BCF LCD_CNTRL, RS ; Set LCD for command mode
00487 BSF LCD_CNTRL, RW ; Setup to read busy flag
00488 BCF LCD_CNTRL, E ; Set E high
00489 BCF LCD_CNTRL, E ; Set E low
00490 MOVF LCD_DATA, w ; Read busy flag, DDRam address
00491 MOVWF TEMP ;
00492 BTFSF TEMP, 7 ; Check busy flag, high=busy
00493 GOTO BUSY_CHECK
00494 BCF LCD_CNTRL, RW ; Select Register Bank1
00495 BSF STATUS, RP0 ;
00496 MOVLW 0x00 ;
00497 MOVWF LCD_DATA_TRIS ; Set port_D for output
00498 BCF STATUS, RP0 ; Select Register Bank0
00499 RETURN
00500 ;
00501 endif

```

```

0083      0093 0782      addwF PCL, F      ; Jump to char pointed to in W reg
0084 344D      retlw 'M'
0085 3469      retlw 'i'
0086 3463      retlw 'c'
0087 3472      retlw 'r'
0088 346F      retlw 'o'
0089 3463      retlw 'c'
008A 3468      retlw 'h'
008B 3469      retlw 'i'
008C 3470      retlw 'p'
008D 3420      retlw '\ '
008E 3454      retlw 'T'
008F 3465      retlw 'e'
0090 3463      retlw 'c'
0091 3468      retlw 'h'
0092 346E      retlw 'n'
0093 346F      retlw 'o'
0094 346C      retlw 'l'
0095 346F      retlw 'o'
0096 3467      retlw 'g'
0097 3479      retlw 'y'
0098      0098 3400      retlw 0

00502      page
00503      ;
00504      Table
00505      addwF PCL, F      ; Jump to char pointed to in W reg
00506      retlw 'M'
00507      retlw 'i'
00508      retlw 'c'
00509      retlw 'r'
00510      retlw 'o'
00511      retlw 'c'
00512      retlw 'h'
00513      retlw 'i'
00514      retlw 'p'
00515      retlw '\ '
00516      retlw 'T'
00517      retlw 'e'
00518      retlw 'c'
00519      retlw 'h'
00520      retlw 'n'
00521      retlw 'o'
00522      retlw 'l'
00523      retlw 'o'
00524      retlw 'g'
00525      retlw 'y'
00526      Table_End
00527      retlw 0
00528      ;
00529      if ( (Table & 0x0FF) >= (Table_End & 0x0FF) )
00530          MESSG "Warning - User Defined: Table Table crosses page boundary in computed jump"
00531      endif
00532      ;
00533
00534
00535
00536      end

```

MEMORY USAGE MAP ('X' = Used, '-' = Unused)

0000 : X--XXXXXXXXXXXX XXXXXXXXXXXXXXXXXXXX XXXXXXXXXXXXXXXXXXXX XXXXXXXXXXXXXXXXXXXX
0040 : XXXXXXXXXXXXXXX XXXXXXXXXXXXXXXXXXXX XXXXXXXXXXXXXXXXXXXX XXXXXXXXXXXXXXXXXXXX
0080 : XXXXXXXXXXXXXXX XXXXXXXXXXXX-----XX

All other memory blocks unused.

Program Memory Words Used: 150
Program Memory Words Free: 1898

Errors : 0
Warnings : 0 reported, 0 suppressed
Messages : 0 reported, 12 suppressed



WORLDWIDE SALES AND SERVICE

AMERICAS

Corporate Office

2355 West Chandler Blvd.
Chandler, AZ 85224-6199
Tel: 480-792-7200
Fax: 480-792-7277
Technical Support: 480-792-7627
Web Address: <http://www.microchip.com>

Atlanta

3780 Mansell Road, Suite 130
Alpharetta, GA 30022
Tel: 770-640-0034
Fax: 770-640-0307

Boston

2 Lan Drive, Suite 120
Westford, MA 01886
Tel: 978-692-3848
Fax: 978-692-3821

Chicago

333 Pierce Road, Suite 180
Itasca, IL 60143
Tel: 630-285-0071
Fax: 630-285-0075

Dallas

4570 Westgrove Drive, Suite 160
Addison, TX 75001
Tel: 972-818-7423
Fax: 972-818-2924

Detroit

Tri-Atria Office Building
32255 Northwestern Highway, Suite 190
Farmington Hills, MI 48334
Tel: 248-538-2250
Fax: 248-538-2260

Kokomo

2767 S. Albright Road
Kokomo, IN 46902
Tel: 765-864-8360
Fax: 765-864-8387

Los Angeles

18201 Von Karman, Suite 1090
Irvine, CA 92612
Tel: 949-263-1888
Fax: 949-263-1338

Phoenix

2355 West Chandler Blvd.
Chandler, AZ 85224-6199
Tel: 480-792-7966
Fax: 480-792-4338

San Jose

1300 Terra Bella Avenue
Mountain View, CA 94043
Tel: 650-215-1444

Toronto

6285 Northam Drive, Suite 108
Mississauga, Ontario L4V 1X5, Canada
Tel: 905-673-0699
Fax: 905-673-6509

ASIA/PACIFIC

Australia

Suite 22, 41 Rawson Street
Epping 2121, NSW
Australia
Tel: 61-2-9868-6733
Fax: 61-2-9868-6755

China - Beijing

Unit 706B
Wan Tai Bei Hai Bldg.
No. 6 Chaoyangmen Bei Str.
Beijing, 100027, China
Tel: 86-10-85282100
Fax: 86-10-85282104

China - Chengdu

Rm. 2401-2402, 24th Floor,
Ming Xing Financial Tower
No. 88 TIDU Street
Chengdu 610016, China
Tel: 86-28-86766200
Fax: 86-28-86766599

China - Fuzhou

Unit 28F, World Trade Plaza
No. 71 Wusi Road
Fuzhou 350001, China
Tel: 86-591-7503506
Fax: 86-591-7503521

China - Hong Kong SAR

Unit 901-6, Tower 2, Metroplaza
223 Hing Fong Road
Kwai Fong, N.T., Hong Kong
Tel: 852-2401-1200
Fax: 852-2401-3431

China - Shanghai

Room 701, Bldg. B
Far East International Plaza
No. 317 Xian Xia Road
Shanghai, 200051
Tel: 86-21-6275-5700
Fax: 86-21-6275-5060

China - Shenzhen

Rm. 1812, 18/F, Building A, United Plaza
No. 5022 Binhe Road, Futian District
Shenzhen 518033, China
Tel: 86-755-82901380
Fax: 86-755-8295-1393

China - Shunde

Room 401, Hongjian Building
No. 2 Fengxiangnan Road, Ronggui Town
Shunde City, Guangdong 528303, China
Tel: 86-765-8395507 Fax: 86-765-8395571

China - Qingdao

Rm. B505A, Fullhope Plaza,
No. 12 Hong Kong Central Rd.
Qingdao 266071, China
Tel: 86-532-5027355 Fax: 86-532-5027205

India

Divyasree Chambers
1 Floor, Wing A (A3/A4)
No. 11, O'Shaughnessey Road
Bangalore, 560 025, India
Tel: 91-80-2290061 Fax: 91-80-2290062

Japan

Benex S-1 6F
3-18-20, Shinyokohama
Kohoku-Ku, Yokohama-shi
Kanagawa, 222-0033, Japan
Tel: 81-45-471-6166 Fax: 81-45-471-6122

Korea

168-1, Youngbo Bldg. 3 Floor
Samsung-Dong, Kangnam-Ku
Seoul, Korea 135-882
Tel: 82-2-554-7200 Fax: 82-2-558-5932 or
82-2-558-5934

Singapore

200 Middle Road
#07-02 Prime Centre
Singapore, 188980
Tel: 65-6334-8870 Fax: 65-6334-8850

Taiwan

Kaohsiung Branch
30F - 1 No. 8
Min Chuan 2nd Road
Kaohsiung 806, Taiwan
Tel: 886-7-536-4818
Fax: 886-7-536-4803

Taiwan

Taiwan Branch
11F-3, No. 207
Tung Hua North Road
Taipei, 105, Taiwan
Tel: 886-2-2717-7175 Fax: 886-2-2545-0139

EUROPE

Austria

Durisolstrasse 2
A-4600 Wels
Austria
Tel: 43-7242-2244-399
Fax: 43-7242-2244-393

Denmark

Regus Business Centre
Lautrup hoj 1-3
Ballerup DK-2750 Denmark
Tel: 45-4420-9895 Fax: 45-4420-9910

France

Parc d'Activite du Moulin de Massy
43 Rue du Saule Trapu
Batiment A - 1er Etage
91300 Massy, France
Tel: 33-1-69-53-63-20
Fax: 33-1-69-30-90-79

Germany

Steinheilstrasse 10
D-85737 Ismaning, Germany
Tel: 49-89-627-144-0
Fax: 49-89-627-144-44

Italy

Via Quasimodo, 12
20025 Legnano (MI)
Milan, Italy
Tel: 39-0331-742611
Fax: 39-0331-466781

Netherlands

P. A. De Biesbosch 14
NL-5152 SC Drunen, Netherlands
Tel: 31-416-690399
Fax: 31-416-690340

United Kingdom

505 Eskdale Road
Winnersh Triangle
Wokingham
Berkshire, England RG41 5TU
Tel: 44-118-921-5869
Fax: 44-118-921-5820

11/24/03